

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

SHIMIZU et al.

Atty. Ref.: 723-1403

Serial No. 10/600,585

Group: TBA

Filed: June 23, 2003

Examiner: TBA

For: EXTERNAL INTERFACES FOR A 3D GRAPHICS SYSTEM

* * * * *

September 8, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

Listed on accompanying Forms PTO-1449 are documents that may be considered material to the examination of this application, in compliance with the duty of disclosure requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98. References listed on Forms PTO-1449 pages 5-9 are provided on the enclosed CD-ROM. The CD-ROM does not include the listed textbook references. These textbooks were filed in Application No.

09/722,382, filed November 28, 2000.

Applicants have listed publication dates on the attached PTO-1449 based on information presently available to the undersigned. However, the listed publication dates should not be construed as an admission that the information was actually published on the date indicated.

Applicants reserve the right to establish the patentability of the claimed invention over any of the information provided herewith, and/or to prove that this information may not be prior art, and/or to prove that this information may not be enabling for the teachings purportedly offered.

This statement should not be construed as a representation that a search has been made, or that information more material to the examination of the present patent application does not exist. The Examiner is specifically requested not to rely solely on the material submitted herewith. It is further understood that the Examiner will consider

information that had been cited by or submitted to the U.S. Patent and Trademark Office in a prior application relied on under 35 U.S.C. § 120. 1138 OG 37, 38 (May 19, 1992).

Applicants have checked the appropriate boxes below.

1. This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No statement under 37 C.F.R. § 1.97(e) or fee is required. In the event, a first Office Action has been mailed prior to filing of the present Information Disclosure Statement, the Office is requested to treat the present paper as a submission under 37 C.F.R. § 1.97(c) and charge the undersigned's Deposit Account No. 14-1140 for the fee required by 37 C.F.R. § 1.17(p). The present paper is submitted in duplicate for this purpose.
2. This Information Disclosure Statement is being filed more than three months after the U.S. filing date AND after the mailing date of the first Office Action on the merits, but before the mailing date of a Final Rejection or Notice of Allowance.

- a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.
- b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.
- c. Attached is our Check in the amount of \$180.00 in payment of the fee under 37 C.F.R. § 1.17(p).

3. This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee. It is hereby requested that the

Information Disclosure Statement be considered. Attached is our Check No. in the amount of \$ in payment of the fee under 37 C.F.R. § 1.17(i).

- a. I hereby state that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(1).
- b. I hereby state that no item of information in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement. 37 C.F.R. § 1.97(e)(2).
4. Relevance of the non-English language document(s) is discussed in the present specification.
5. The document(s) was/were cited in a corresponding foreign application. An English language version of the foreign search report is attached for the Examiner's information.
6. A concise explanation of the relevance of the non-English language document(s) appears below:
7. The Examiner's attention is directed to co-pending U.S. Patent Application Nos. 10/132,702; 10/225,472; 10/225,487; and 10/225,488, which are directed to related technical subject matter. Copies of these applications are enclosed herewith. The identification and submission of these applications are not to be construed as waivers of secrecy as to these applications now or upon issuance of the subject application as a patent. The Examiner is respectfully requested to consider the cited applications and the art cited therein during examination.
8. Copies of the documents were cited by or submitted to the Office in Application No. , filed , which is relied upon for an earlier filing date under 35 U.S.C. § 120. Thus, copies of these documents are not attached. 37 C.F.R. § 1.98(d).

It is respectfully requested that the Examiner initial and return a copy of the enclosed PTO-1449, and to indicate in the official file wrapper of this patent application that the documents have been considered.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 14-1140 referencing docket number 723-1403.

Respectfully submitted,

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INFORMATION DISCLOSURE

ATTY. DOCKET NO.

SERIAL NO.

723-1403

10/600,585

APPLICANT

SHIMIZU et al.

FILING DATE

GROUP

FEB 05 2004

(Use several sheets if necessary)

June 23, 2003

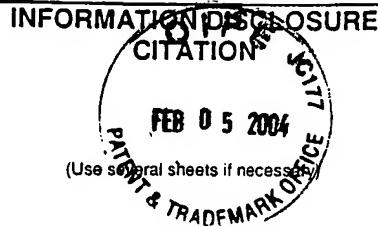
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	5,832,244	11/1998	JOLLEY et al.	395	309	
	5,816,921	10/1998	HOSOKAWA	463	43	

*Examiner

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(Use several sheets if necessary)

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	4,850,591	07/1989	TAKEZAWA et al.	273	85	

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DOCUMENT	DATE	COUNTRY	CLASS	SUBCLAS	YES	NO	
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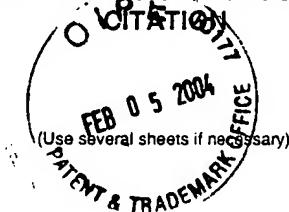
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	<p>HCO8 – 68HC08AS32, Advance Information Rev. 3.0, Motorola, printed out July 24, 2001, pp. 2-27, and 234-308</p>
	<p>Website http://www.repairfaq.org/REPAIR/F_Pinouts.html entitled "Pinouts for various connectors in Real Life(tm)", page 1 of 3, dated 5/20/97, author not established.</p>
	<p>Christy J., Website http://www.repairfaq.org/REPAIR/F_SNES.html entitled "Super Nintendo Entertainment System: pinouts & protocol, 3/26/96, 5 pages by way of link "2.2) SNES" in the website above.</p>
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	<p>U.S. Application No. 10/225,472</p>
	<p>U.S. Application No. 10/225,487</p>
	<p>U.S. Application No. 10/225,488</p>

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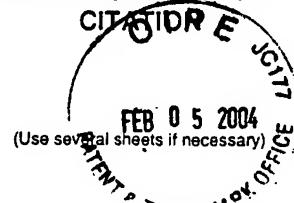
TRANSLATION

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
056	EP 1 081 649	3/2001	EUROPEAN				
057	EP 1 075 146	2/2001	EUROPEAN				
058	EP 1 074 945	2/2001	EUROPEAN				
059	JP 2000-215325	8/2000	JAPAN (w/English Abstract)				
060	JP 2000-207582	7/2000	JAPAN (w/English Abstract)				
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063	JP 2000-149053	5/2000	JAPAN (w/English Abstract)				
064	JP 2000-132706	5/2000	JAPAN (w/English Abstract)				
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	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO
070	JP 11226257	8/1999	JAPAN (w/English Abstract)				
071	JP 11203500	7/1999	JAPAN (w/English Abstract)				
072	JP 11161819	6/1999	JAPAN (w/English Abstract)				
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089	Whitepaper: Optimizing Direct3D for the GeForce 256, 1/3/2000, www.nvidia.com
090	Whitepaper: Dot Product Texture Blending, 12/3/1999, www.nvidia.com
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101	"First PlayStation II Gameplay Screens Revealed!", Next Generation, September 1999
102	Game Enthusiast Online Highlights, March 18, 1999
103	Game Enthusiast Online Highlights, March 19, 1999
104	Game Enthusiast Online Highlights, March 17, 1999
105	Game Enthusiast Online Highlights, October 20, 1999
106	Joel Easley, "PlayStation II Revealed", Game Week, September 29, 1999

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111	AM News: Japanese Developers Not All Sold on PS2, Next Generation, March 16, 1999		
112	Sony To Turn PlayStation Maker Into Wholly Owned Unit-Nikkei, Dow Jones News Service, March 8, 1999		
113	Yumiko Ono, Sony Antes Up Its Chips In Bet On New Game System, Dow Jones News Service, March 4, 1999		
114	MacWeek.Com Gets Inside Story on Connectix VGS for Windows; Controversial Emulator of Sony PlayStation Games Cureently Available for Macs Only, Business Wire, March 12, 1999		
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